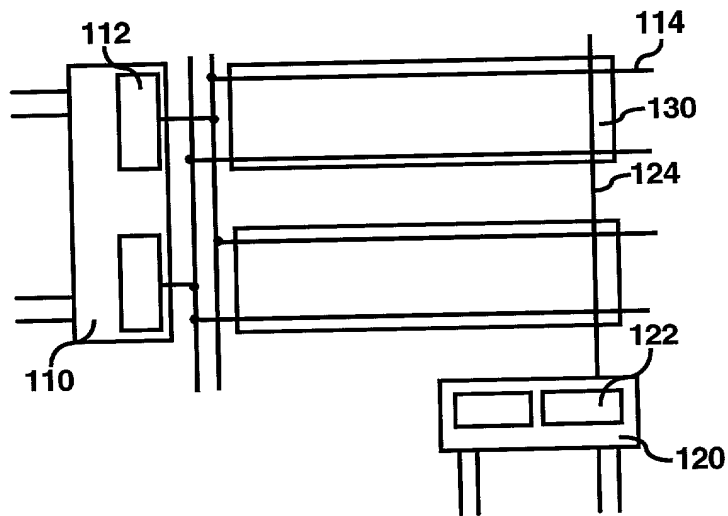


100



**FIG. 1**  
**(PRIOR ART)**

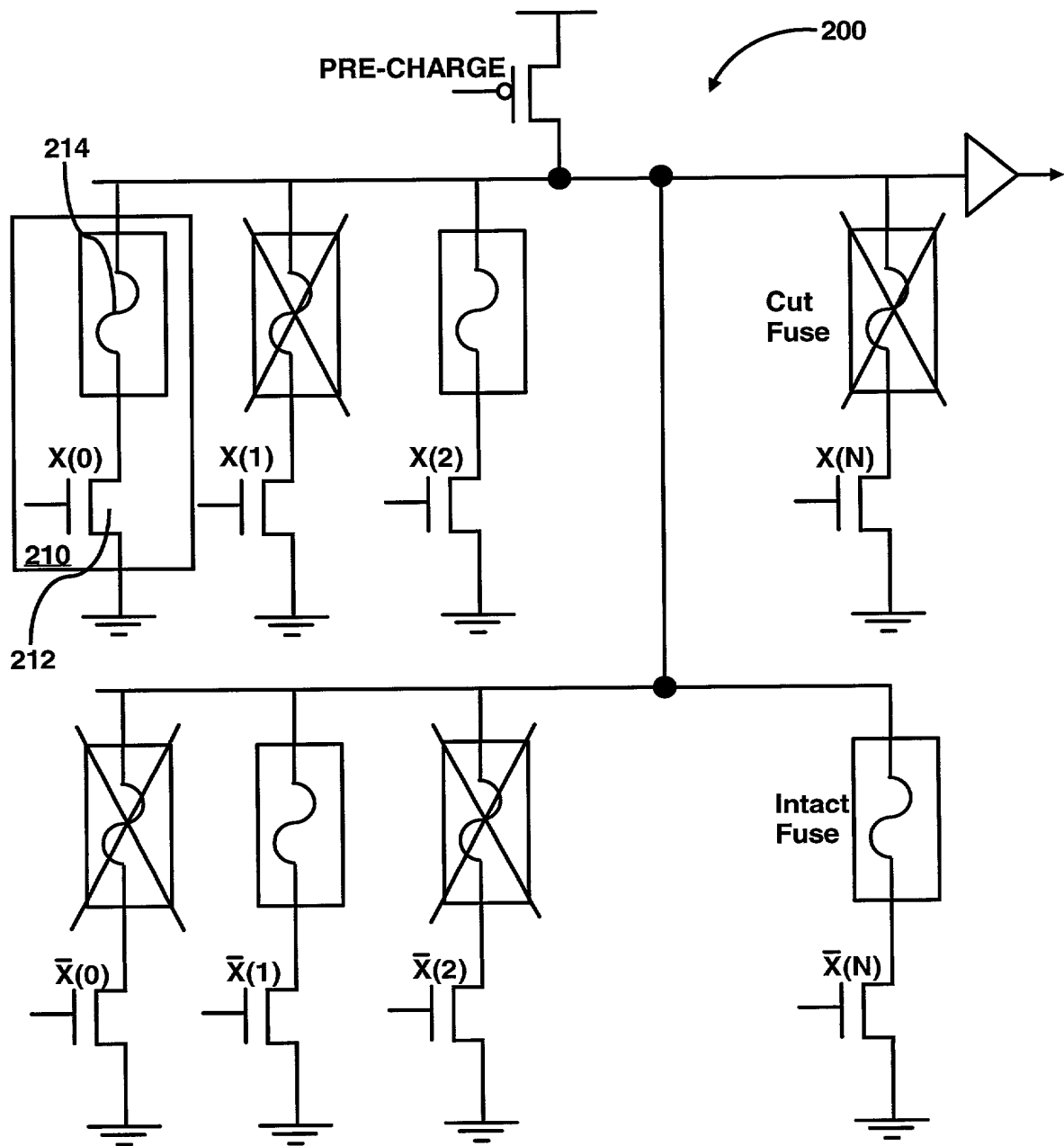
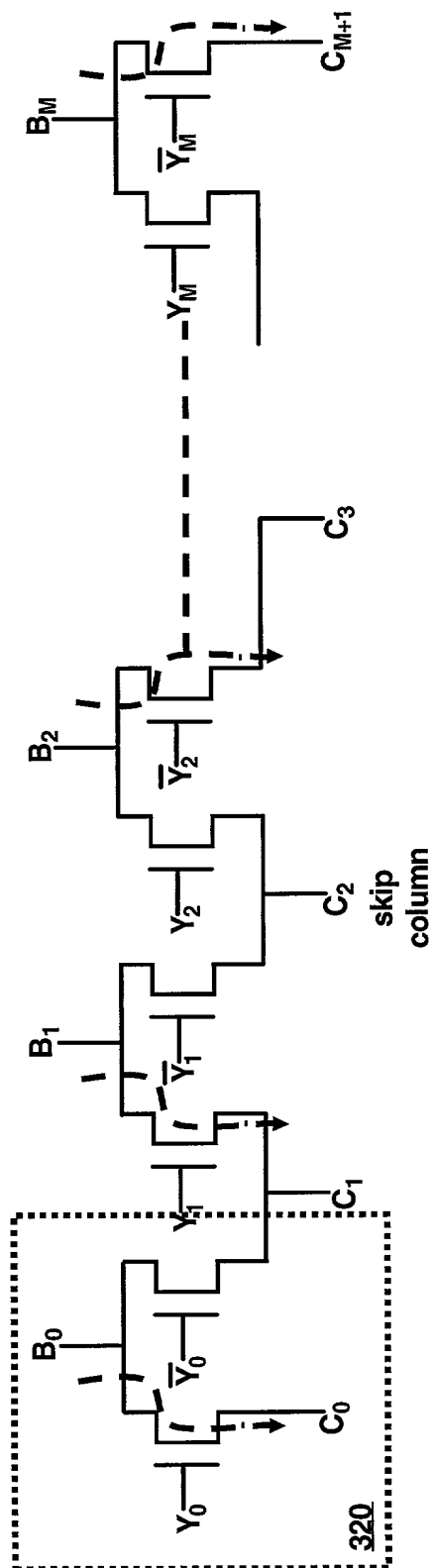
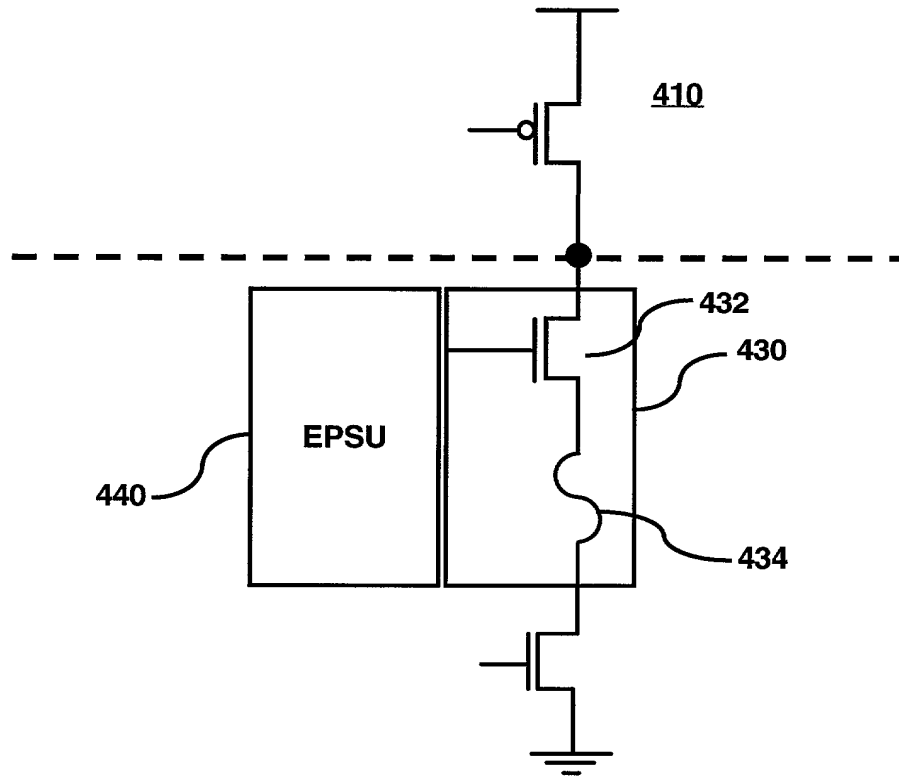


FIG. 2  
(PRIOR ART)

The diagram shows a multi-bit digital-to-analog converter (300) enclosed in a dashed box. It is connected to a reference voltage source  $V_{ref}$  and a ladder of resistors (312). The resistors are connected in series, with the output of the first resistor (312) connected to a node  $Y_0$ . This node  $Y_0$  is connected to an inverter (314) and also to a second resistor. The output of the second resistor is connected to a node  $Y_2$ , which is also connected to an inverter (314). A dashed line indicates that the circuit continues. The final output of the circuit is a digital signal  $Y_M$ , which is connected to an inverter (314) and also to a capacitor and a ground symbol.



**FIG. 3**  
**(PRIOR ART)**



*FIG. 4A*

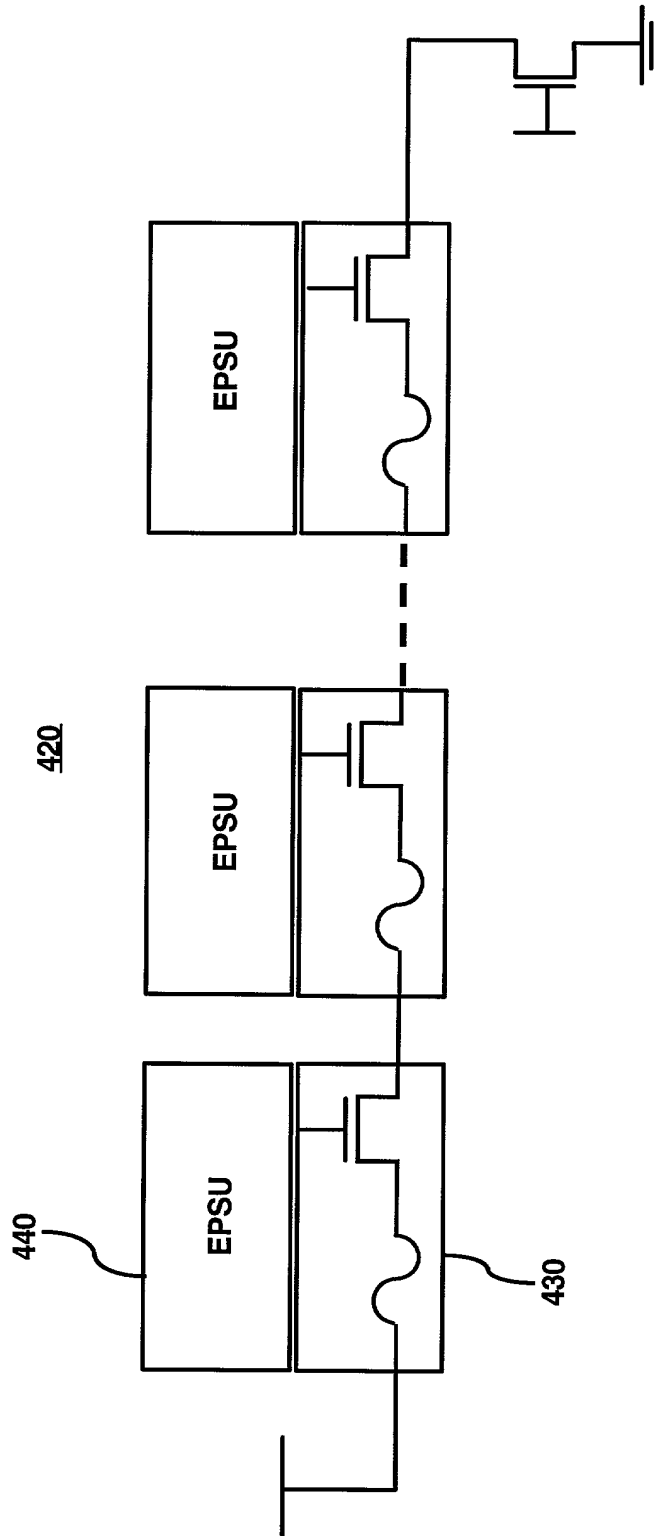
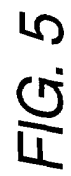
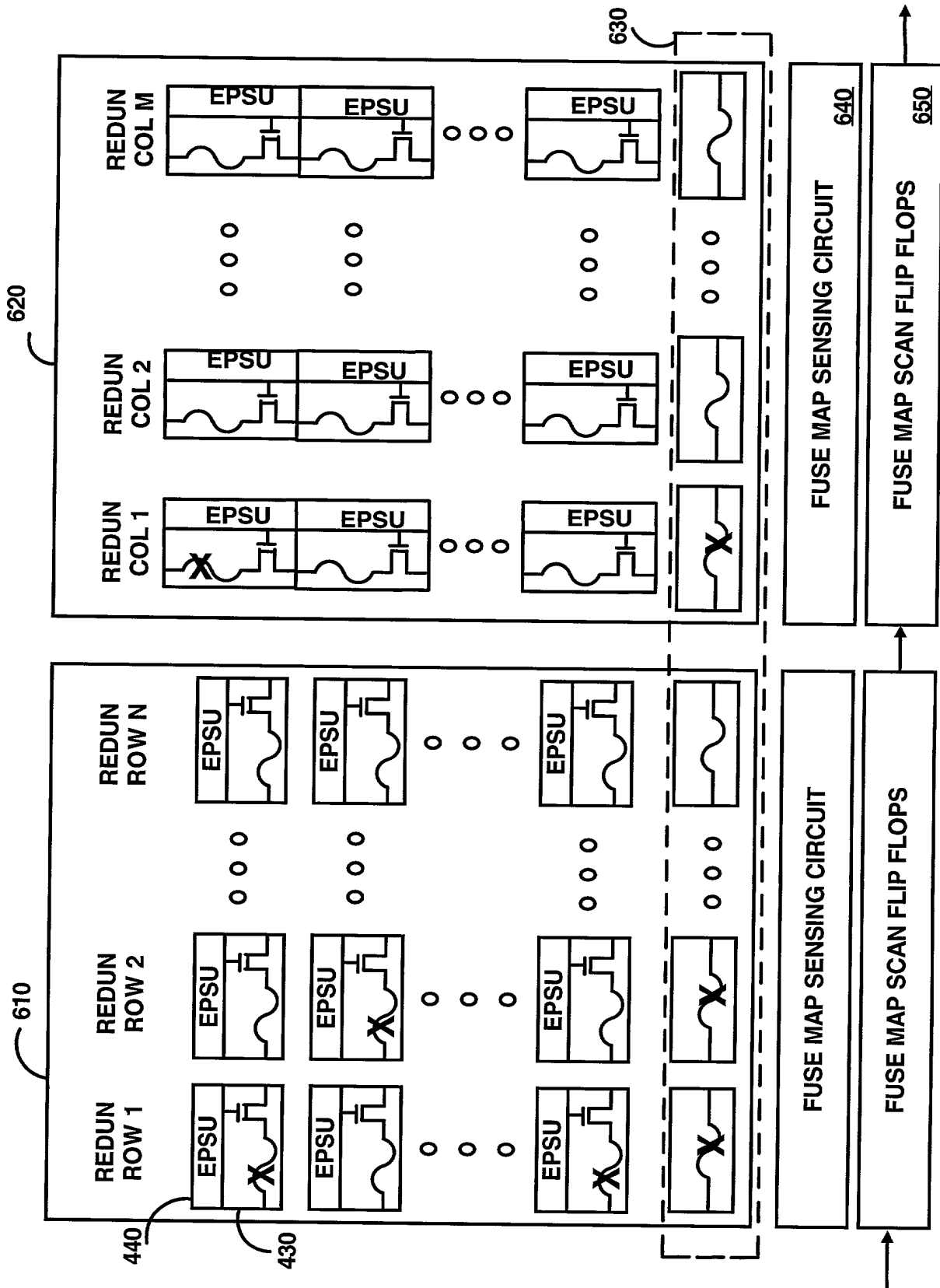


FIG. 4B



**FIG. 5**



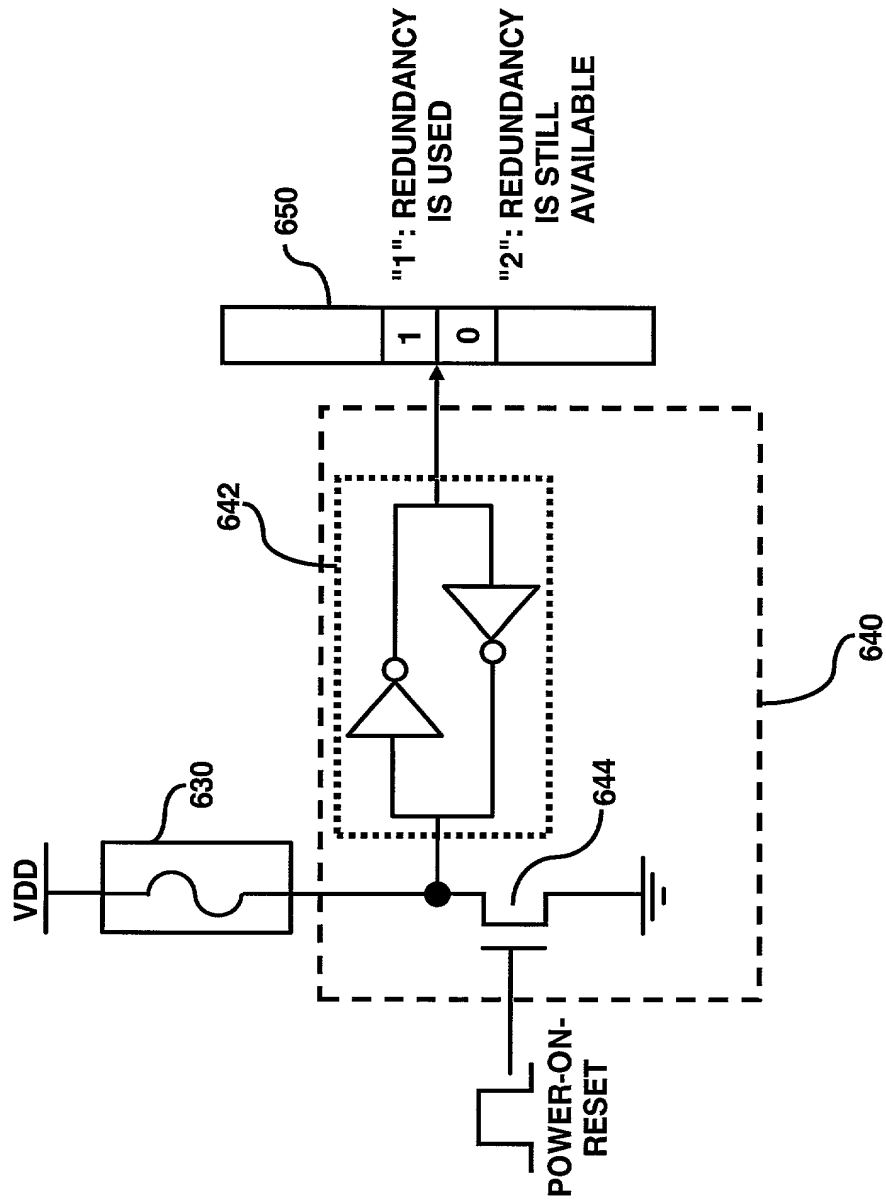


FIG. 6B



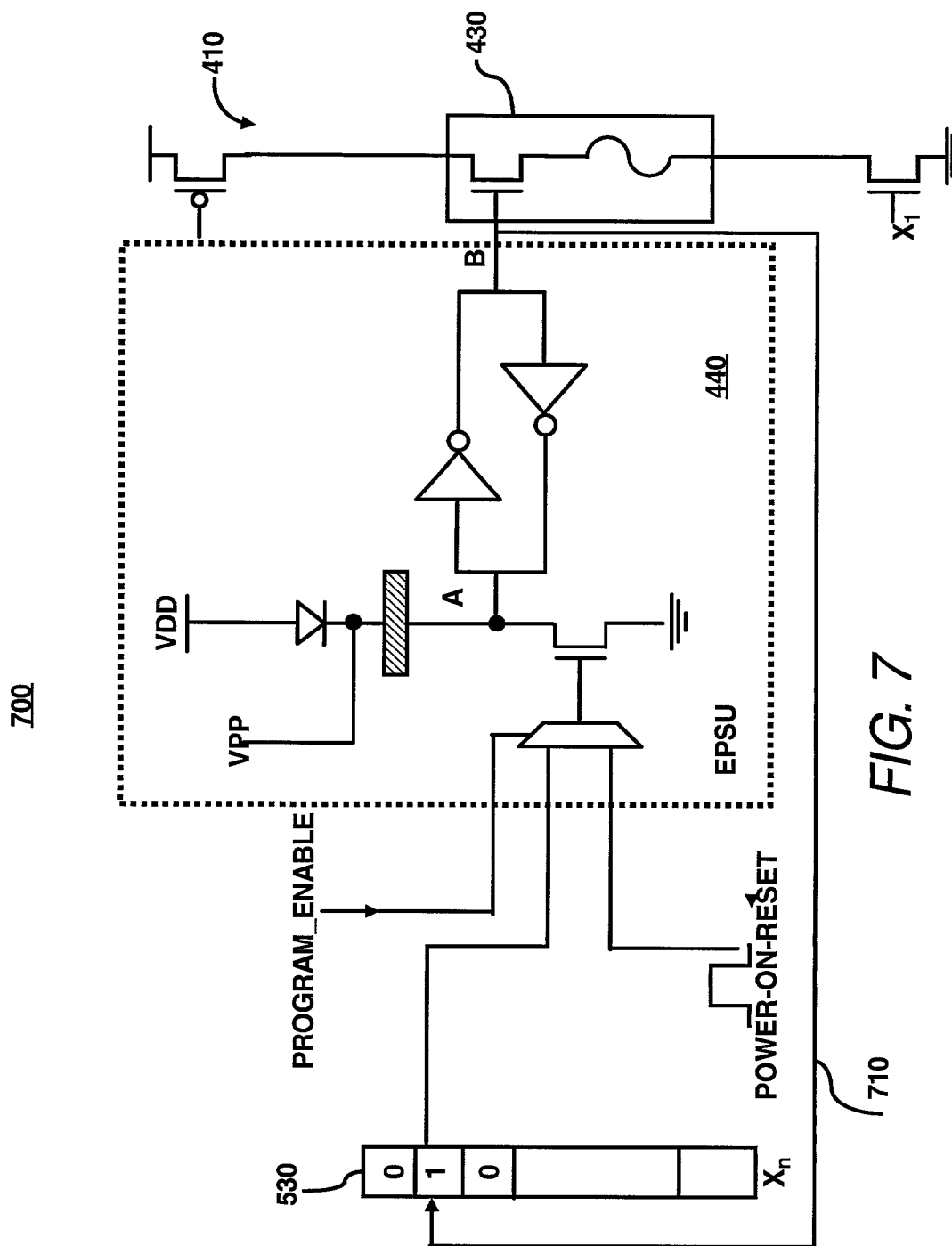
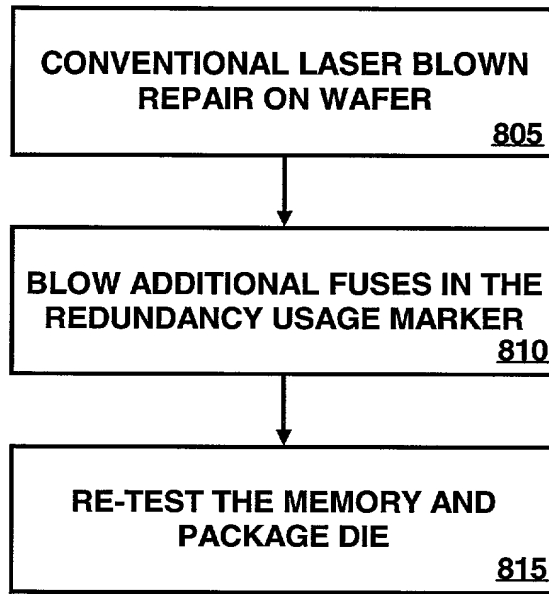
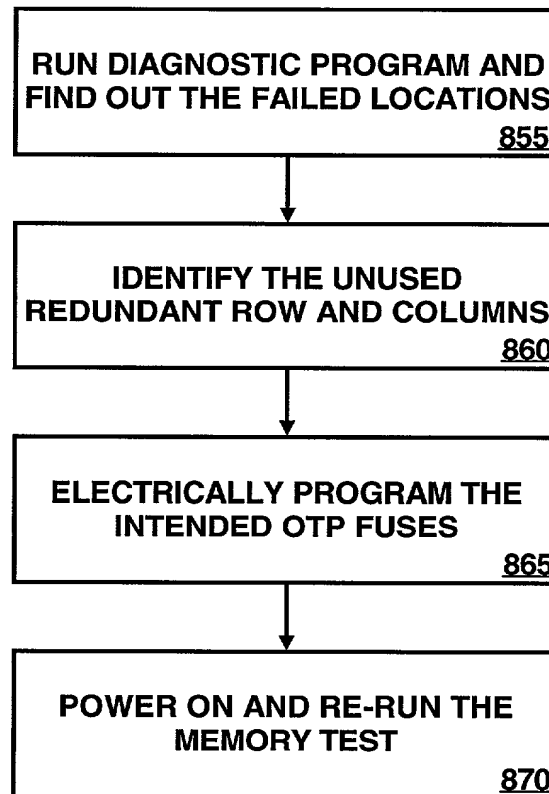


FIG. 7



800

*FIG. 8A*



850

*FIG. 8B*